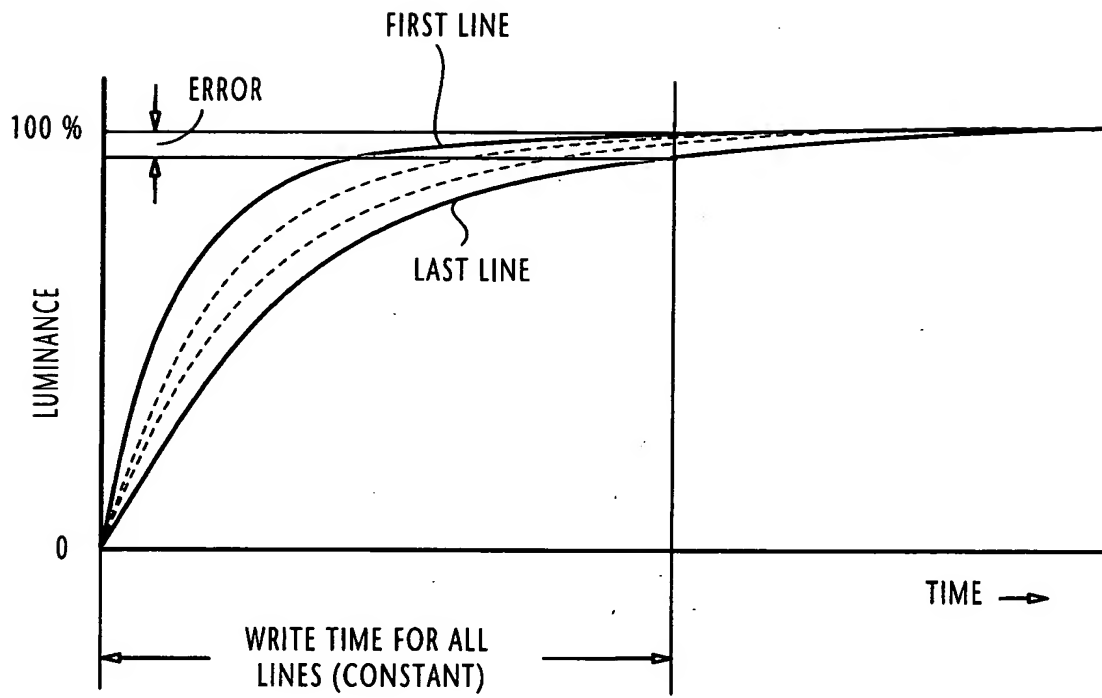


FIG. 1
PRIOR ART



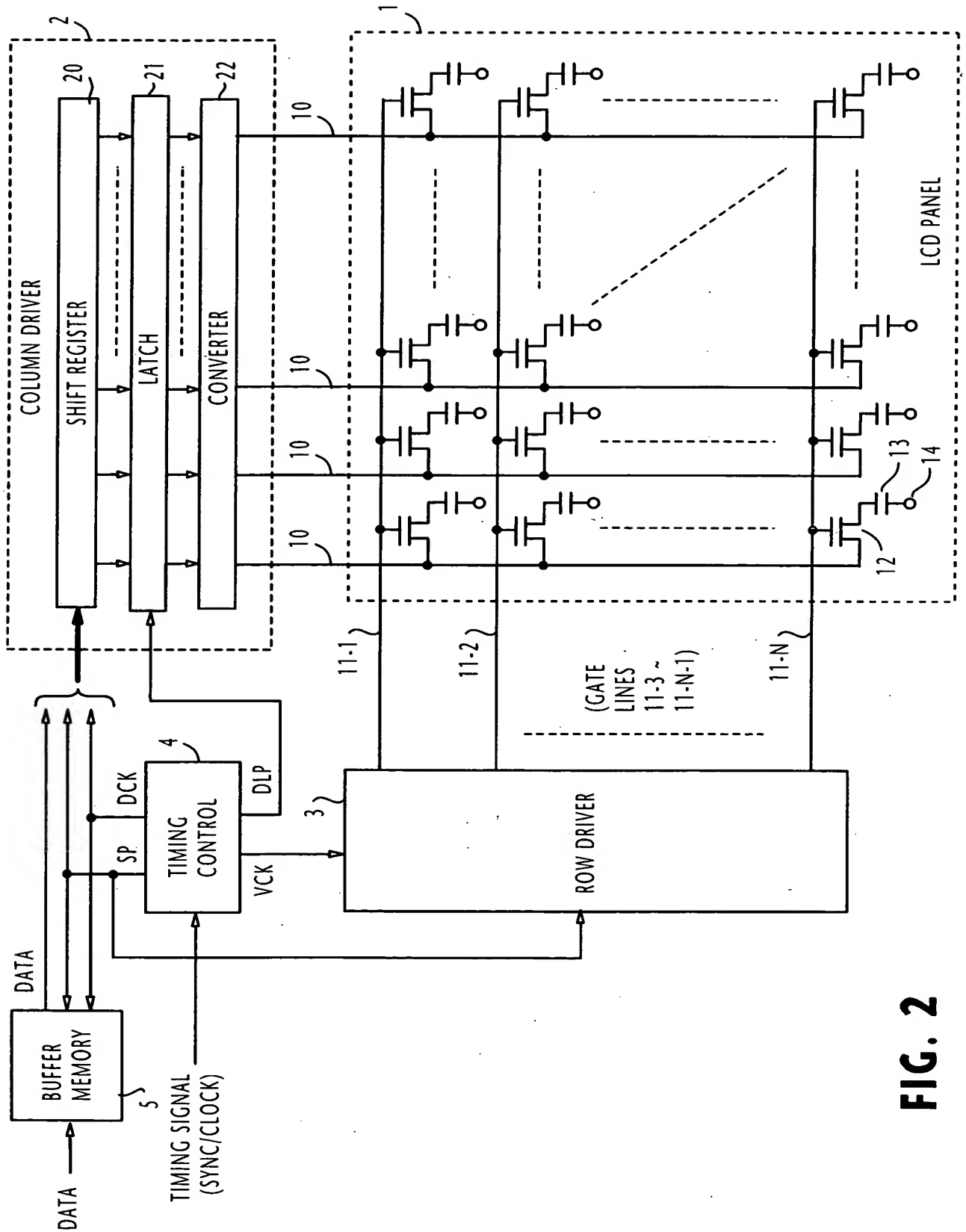


FIG. 2

FIG. 3

TIMING CONTROLLER

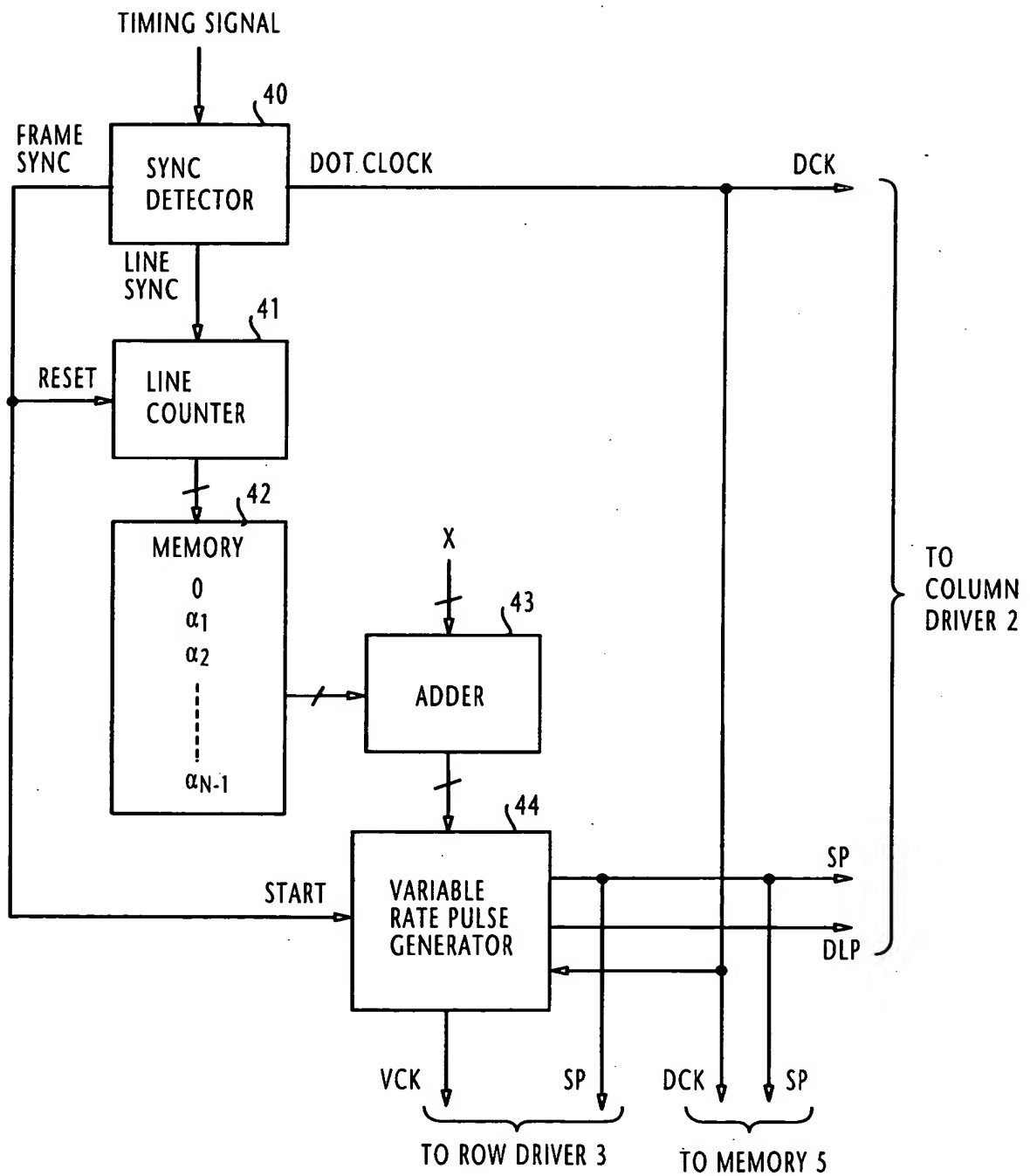


FIG. 4

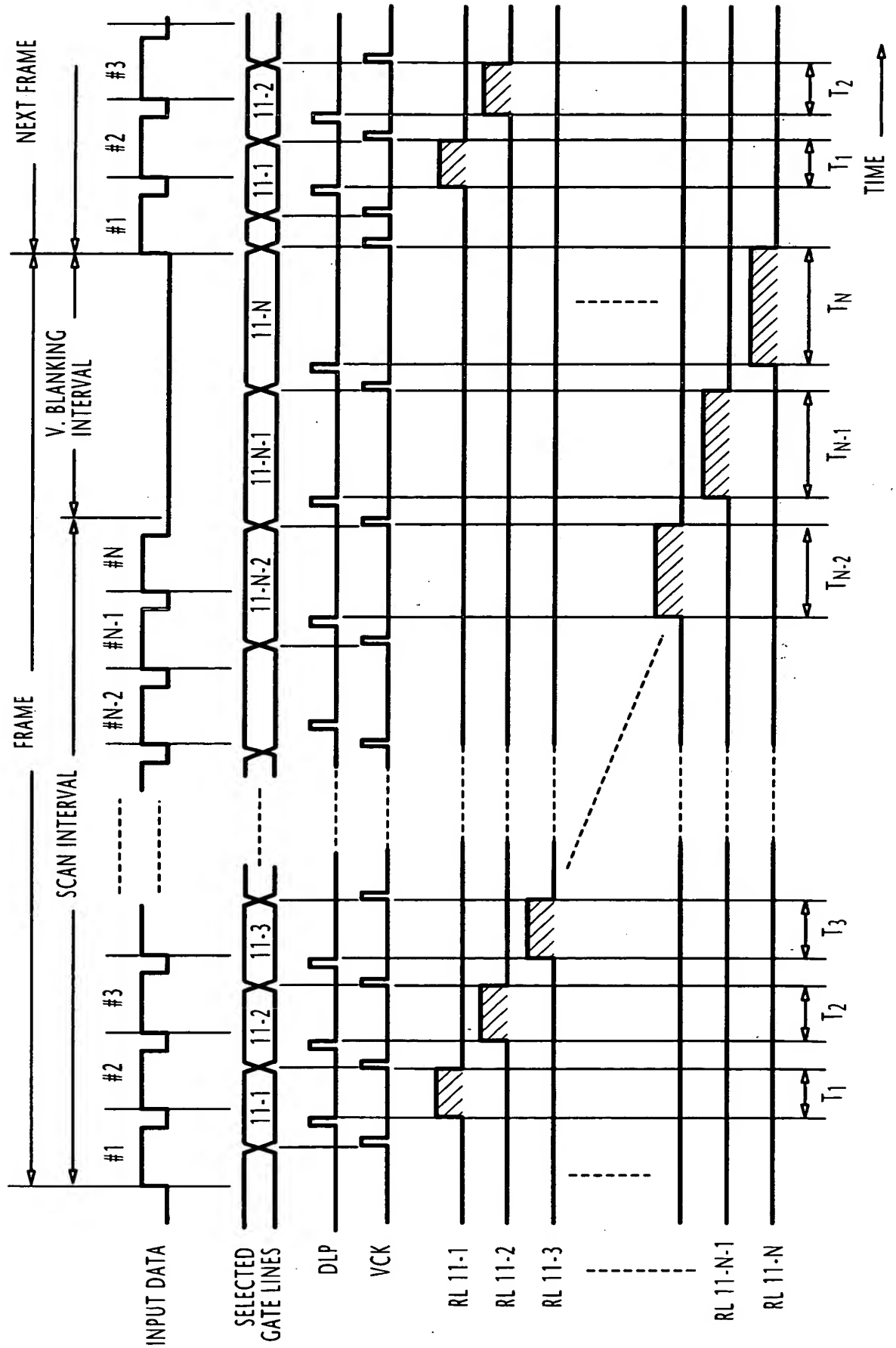


FIG. 5

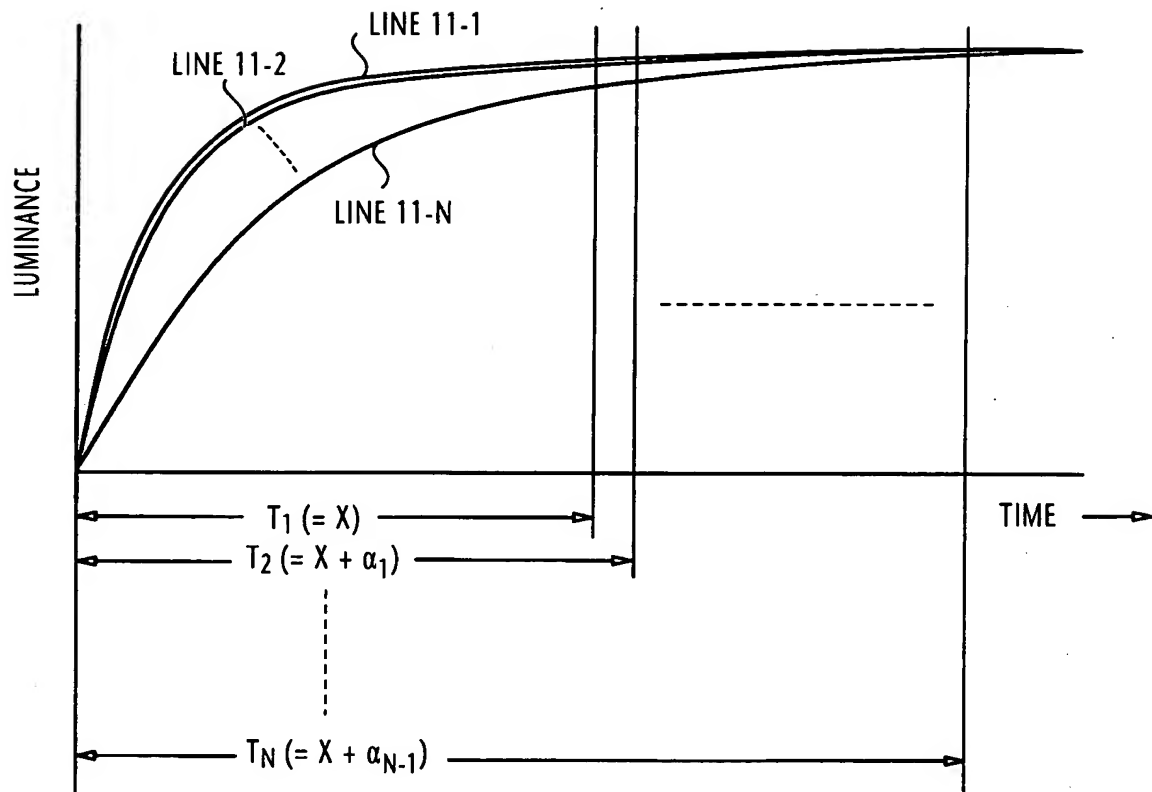


FIG. 6

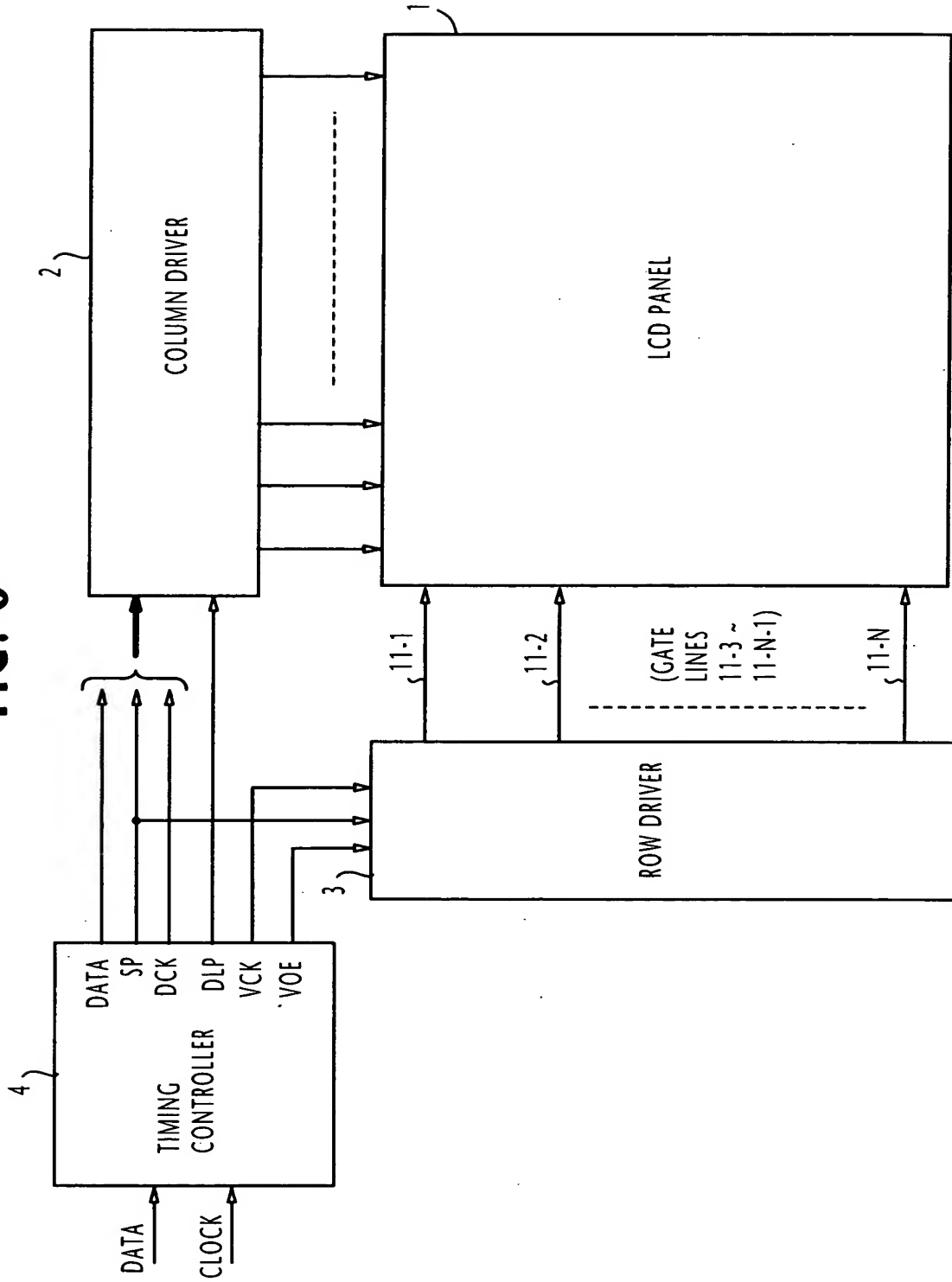


FIG. 7

TIMING CONTROLLER

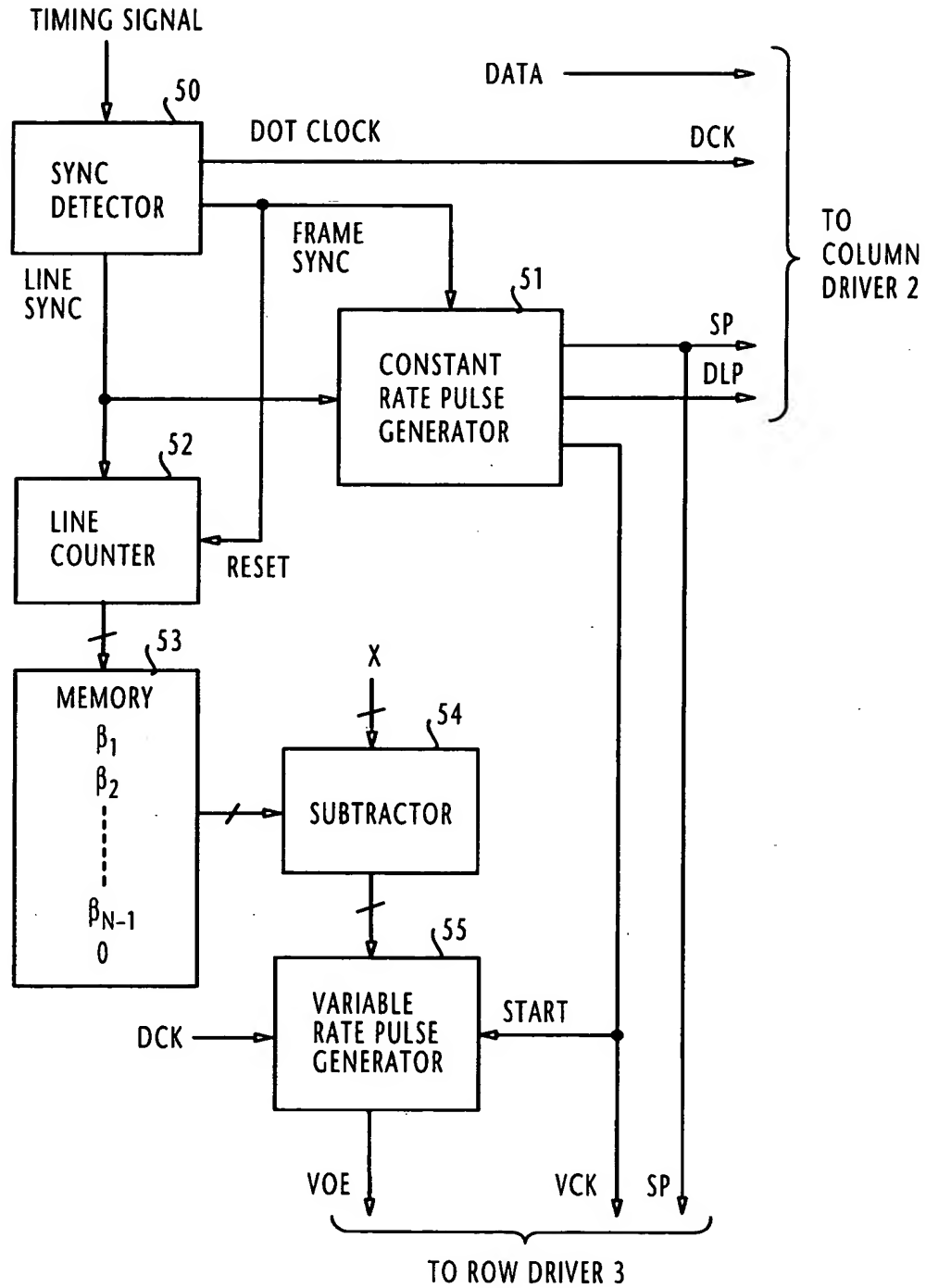


FIG. 8

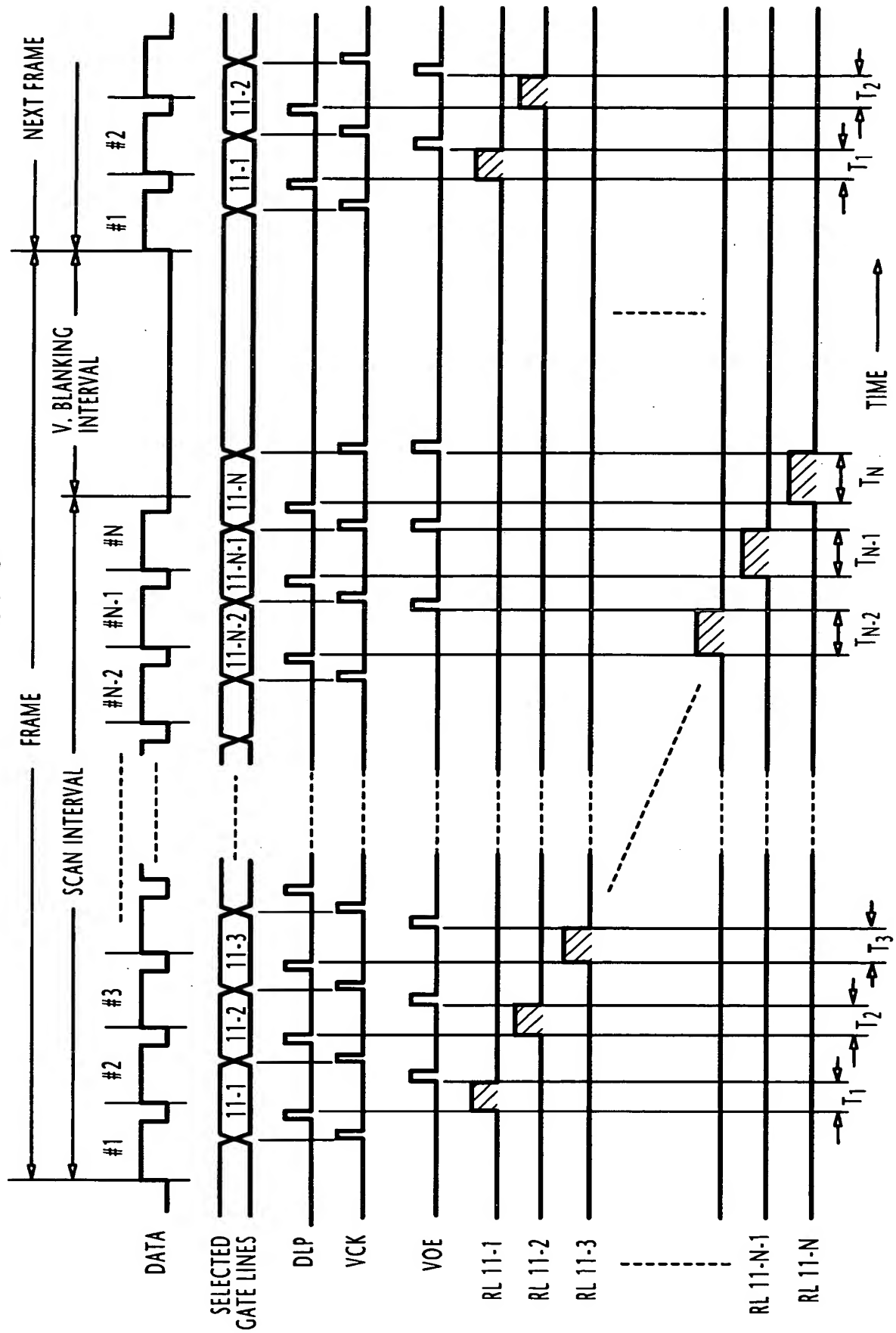


FIG. 9

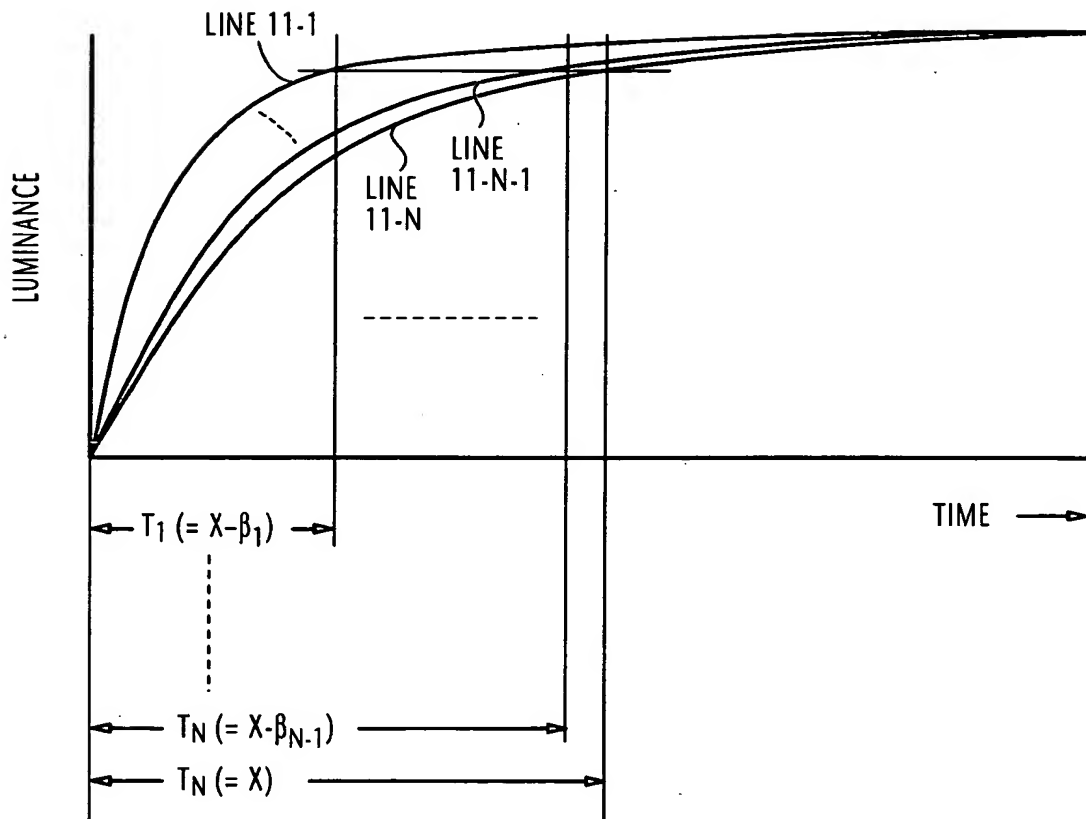


FIG. 10

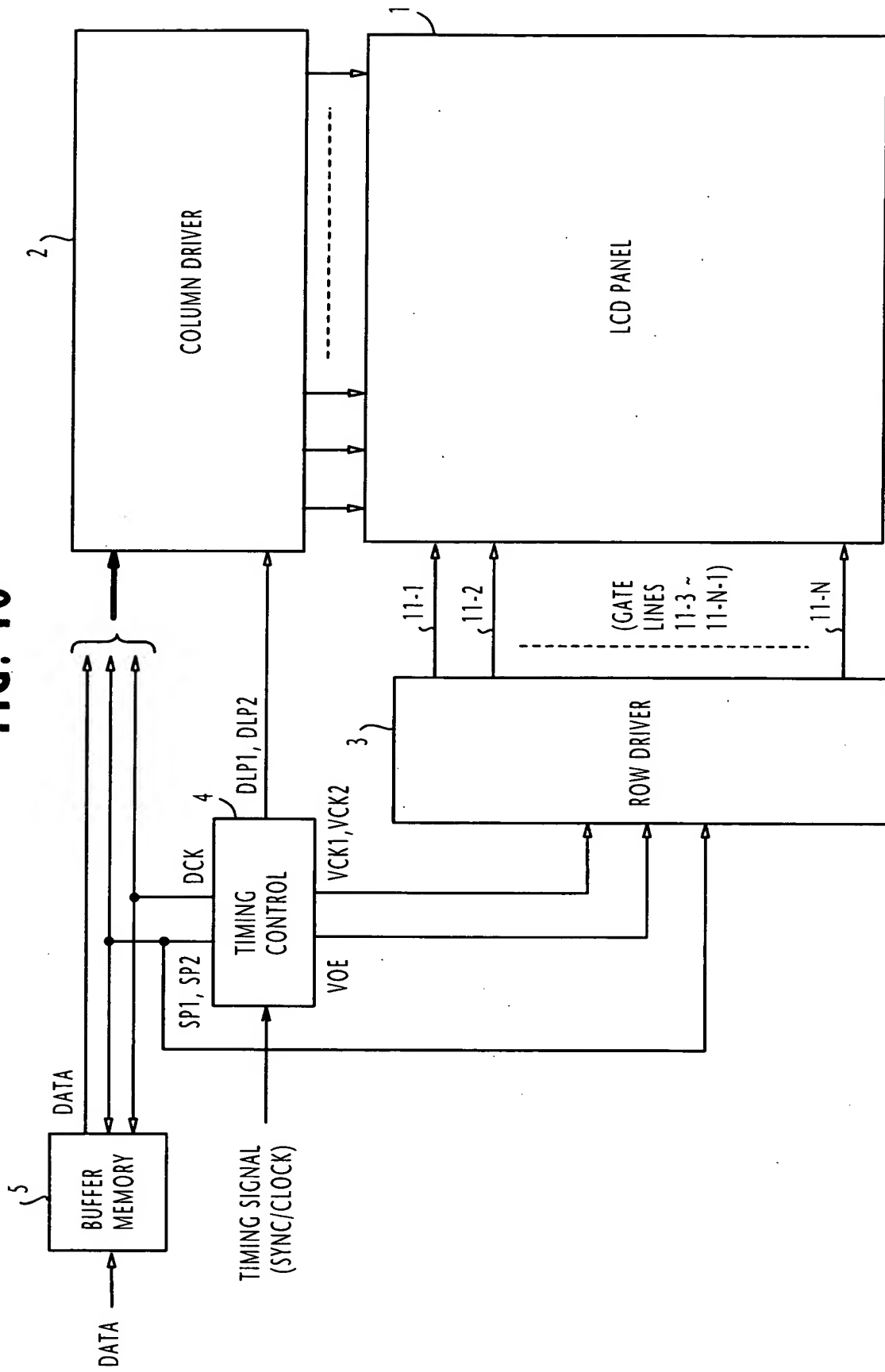


FIG. 11

TIMING CONTROLLER

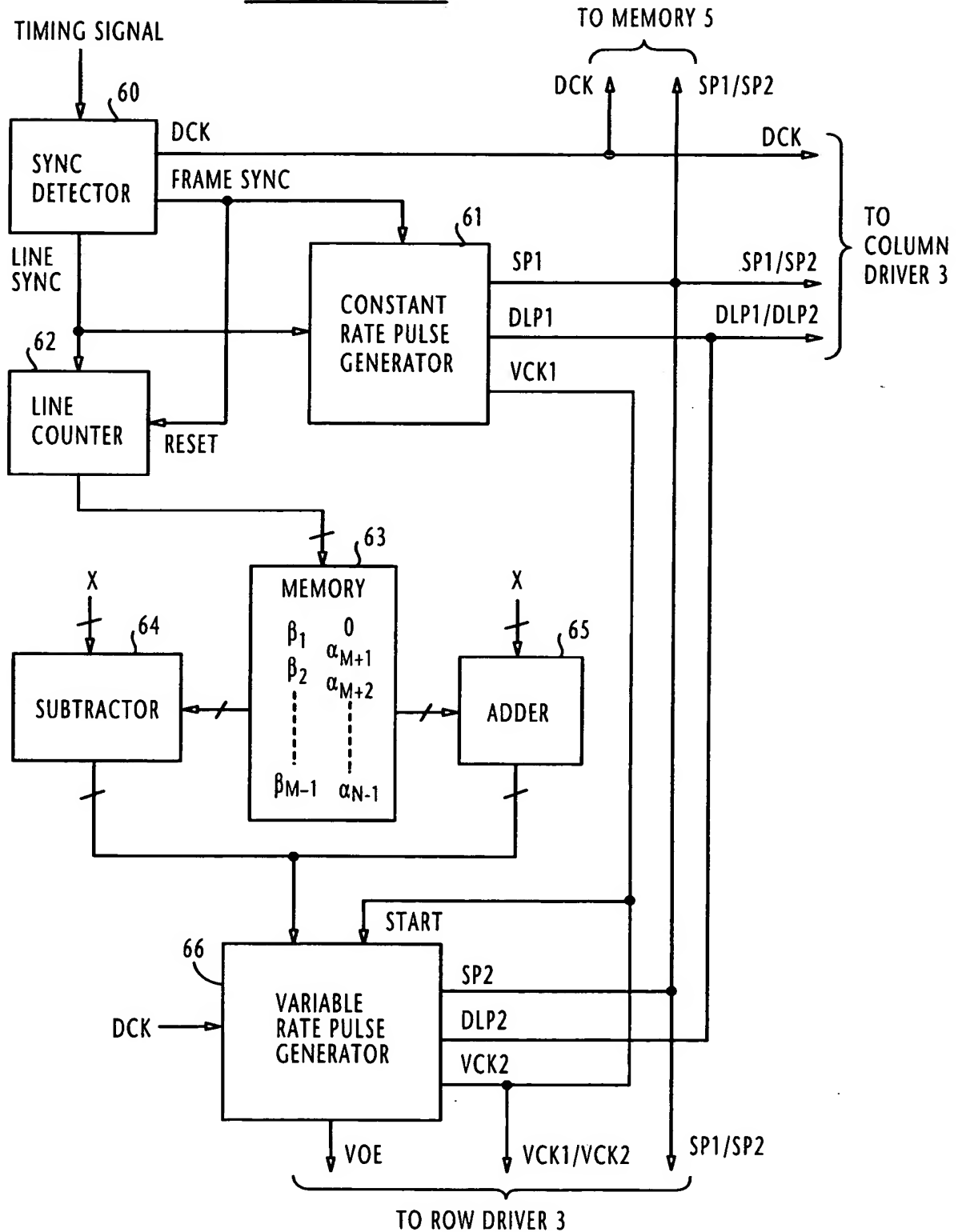


FIG. 12

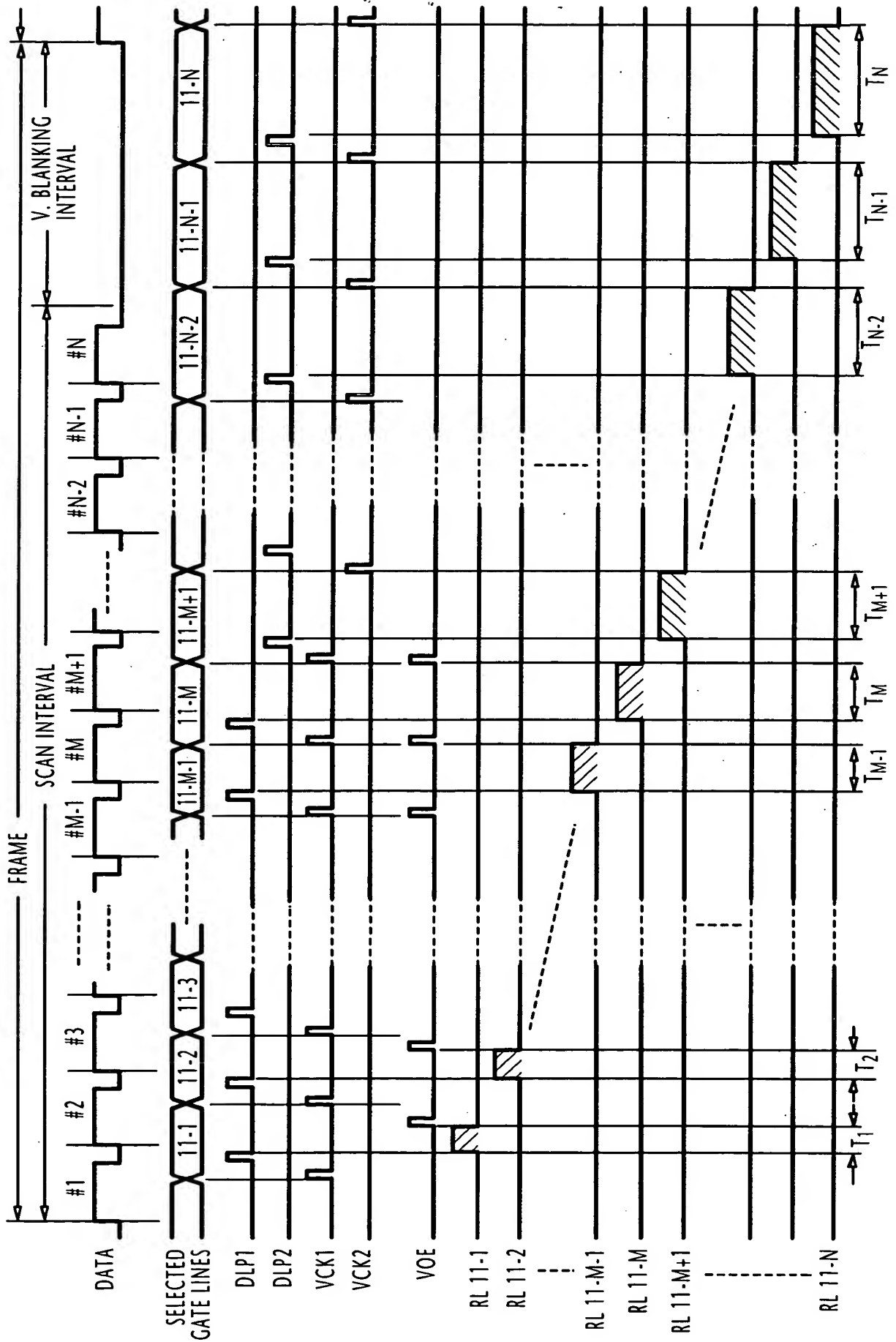


FIG. 13

